

x86 segment descriptor reference

upper dword																				lower dword																									
6	6	6	6	5	5	5	5	5	5	5	5	5	4	4	4	4	4	4	4	3	3	3	3	3	3	3	2	2	2	2	2	2	2	2	2	1	1	1	1		1	1	1	1	1
Base 31:24								G	D	B	L	..	Limit 19:16				P	DPL	S	Type				Base 23:00								Limit 15:00								General format					
Base 31:24								G	B	0	..	Limit 19:16				P	DPL	1	0	E	W	A	Base 23:00								Limit 15:00								Data Segment						
Base 31:24								G	D	0	..	Limit 19:16				P	DPL	1	1	C	R	A	Base 23:00								Limit 15:00								Code Segment (16-/32-bit)						
Reserved								G	D	1	..	Reserved				P	DPL	1	1	C	R	A	Reserved																Code Segment (IA-32e)						
Base 31:24								G	R	0	R	Limit 19:16				P	DPL	0	Type				Base 23:00								Limit 15:00								System Segment						
Offset in Segment 31:16												P	DPL	0	1100				0	0	0	Param count				Segment selector								Offset in Segment 15:00								Call Gate (32-bit)			
Offset in Segment 31:16												P	DPL	0	1100				0	0	0	0	0	0	0	Segment selector								Offset in Segment 15:00								Call Gate (IA-32e)			
Reserved												0000				Reserved				Offset in Segment 63:31																64-bit Call Gate extension (at 8Â byte offset)									
Offset in Segment 31:16												P	DPL	0	D	1	1	0	0	0	0	Reserved				Segment selector								Offset in Segment 15:00								Interrupt Gate			
Offset in Segment 31:16												P	DPL	0	D	1	1	1	0	0	0	Reserved				Segment selector								Offset in Segment 15:00								Trap Gate			
Reserved												P	DPL	0	D	1	0	1	Reserved				TSS Segment selector								Reserved								Task Gate						
Base 31:24								G	0	0	..	Limit 19:16				P	DPL	0	Type				Base 23:00								Limit 15:00								TSS/LDT Descriptor						

by diffie (<https://diffie.eu>)